

Figure 1

RS1 Operand Data Format

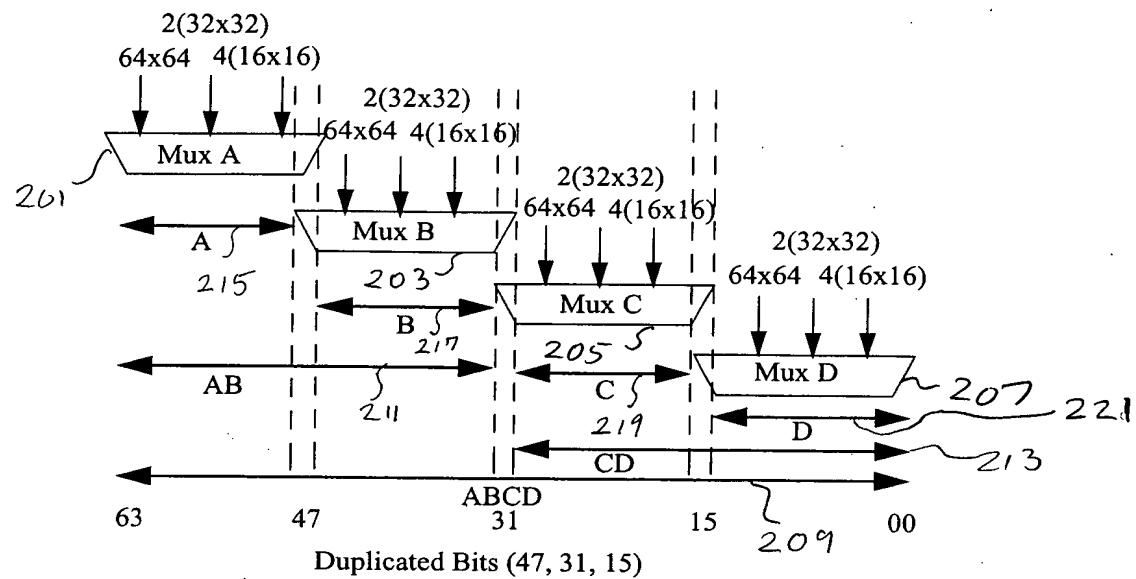
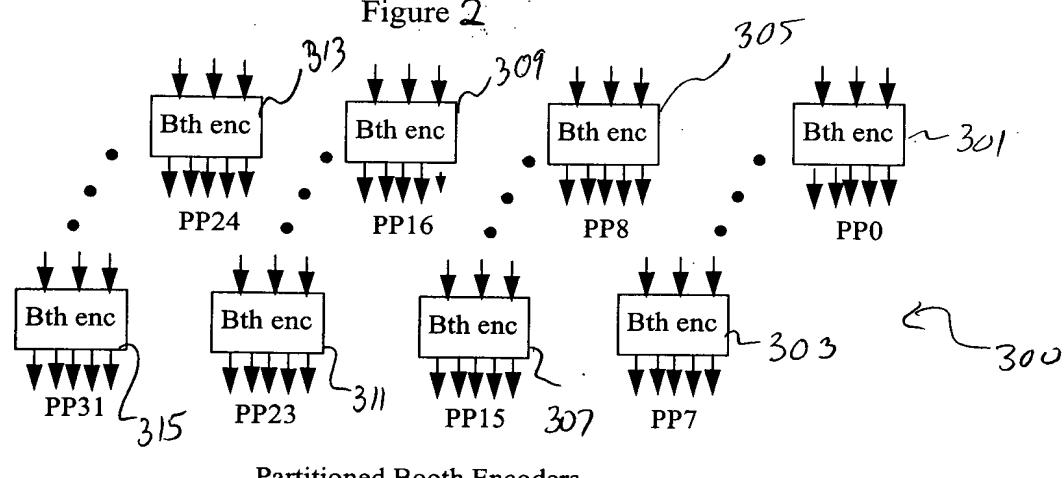


Figure 2



Partitioned Booth Encoders

Figure 3

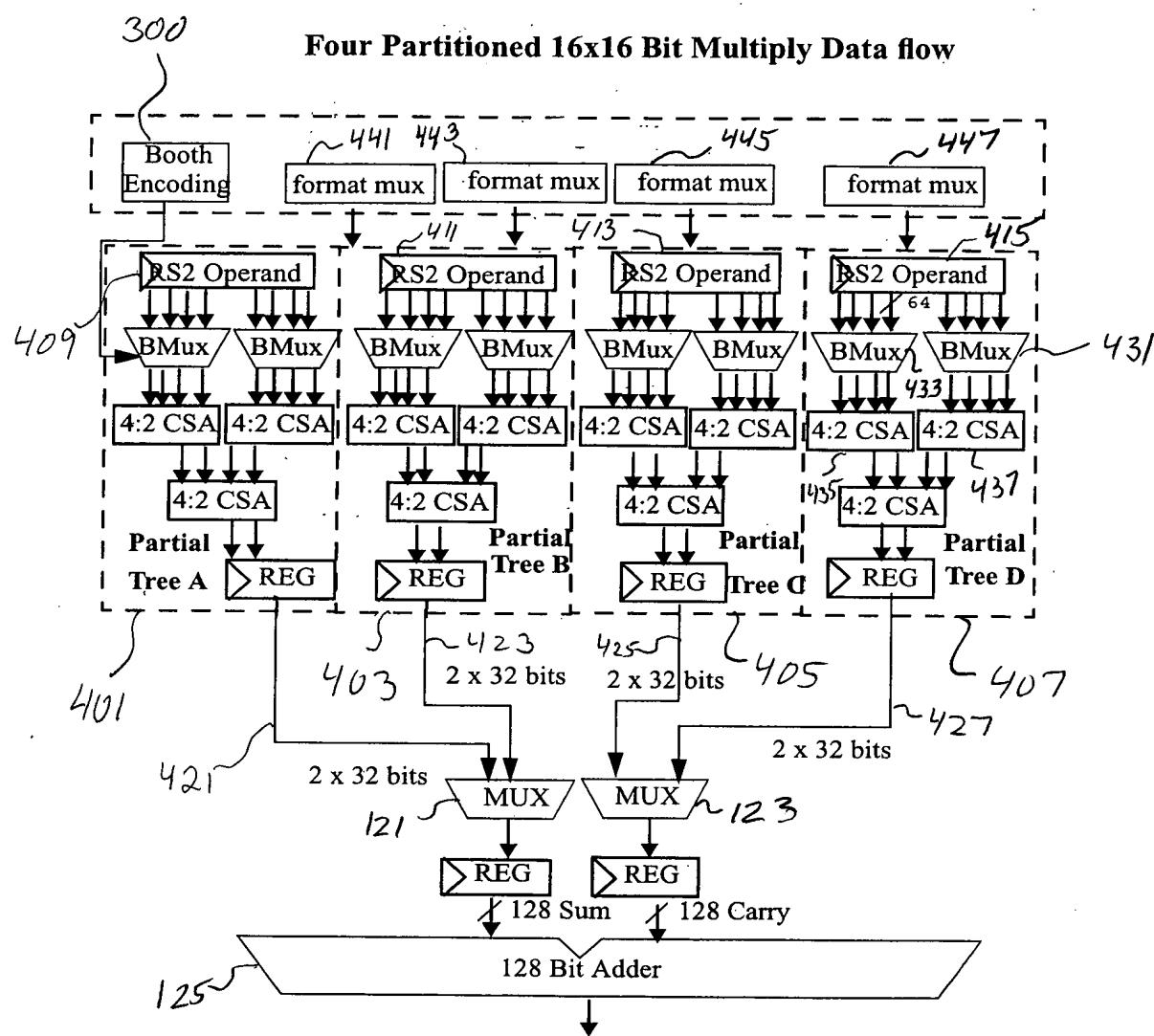


Figure 4

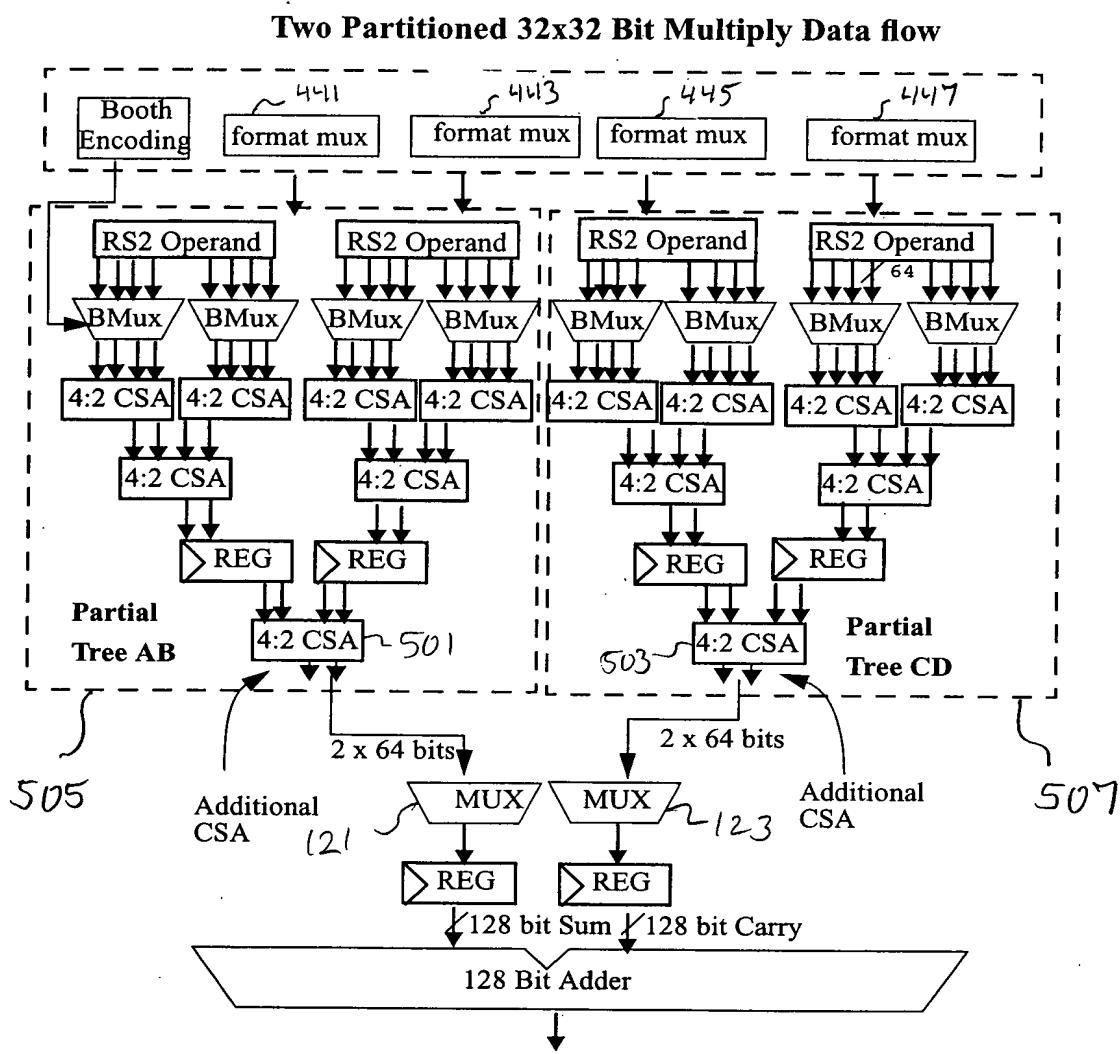


Figure 5

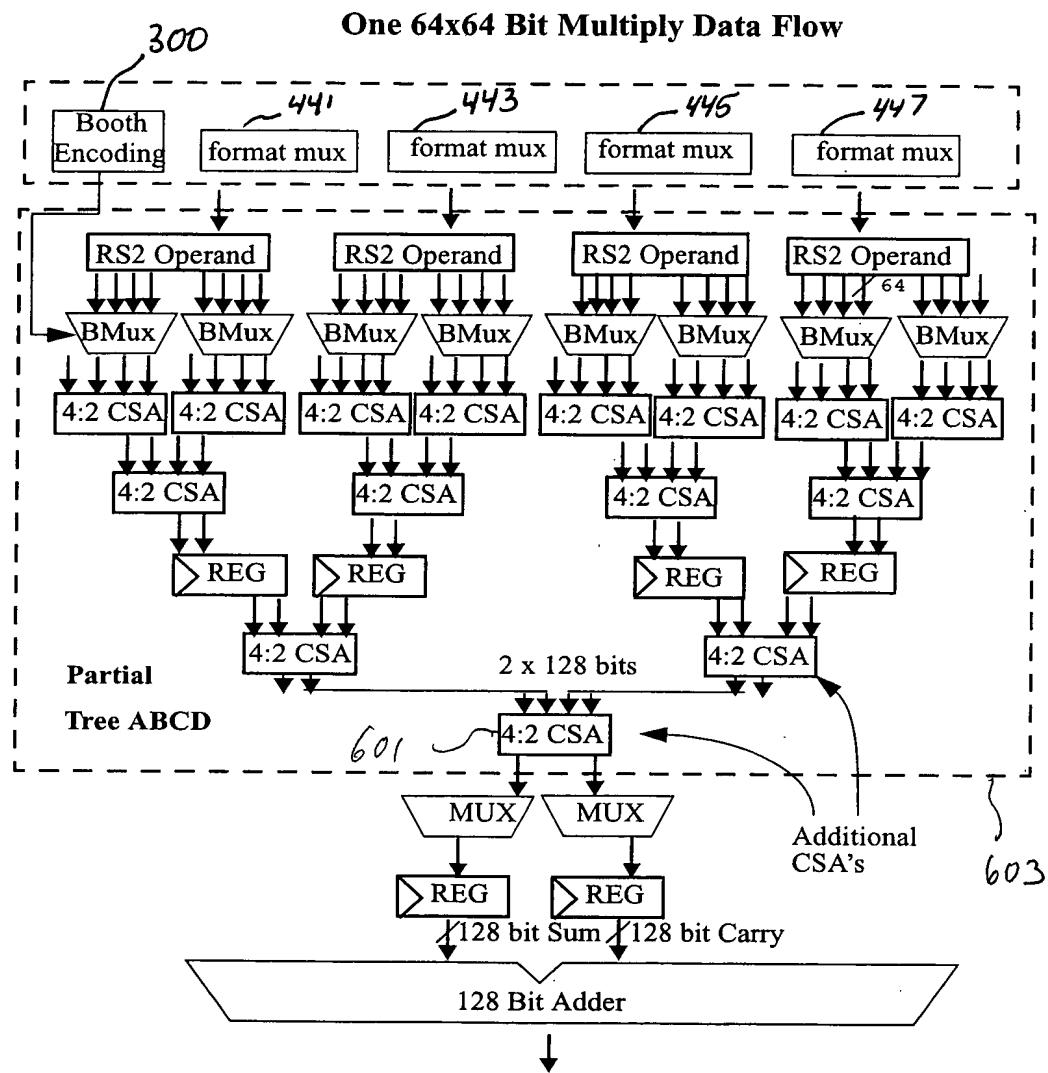


Figure 6

**MULTIPLIER STRUCTURE SUPPORTING DIFFERENT
PRECISION MULTIPLICATION OPERATIONS**
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Four SIMD (16×16) Multiplies

Fig. 7

**MULTIPLIER STRUCTURE SUPPORTING DIFFERENT
PRECISION MULTIPLICATION OPERATIONS**

Two Single Precision (32 x 32) Multiplies

Fig 8

